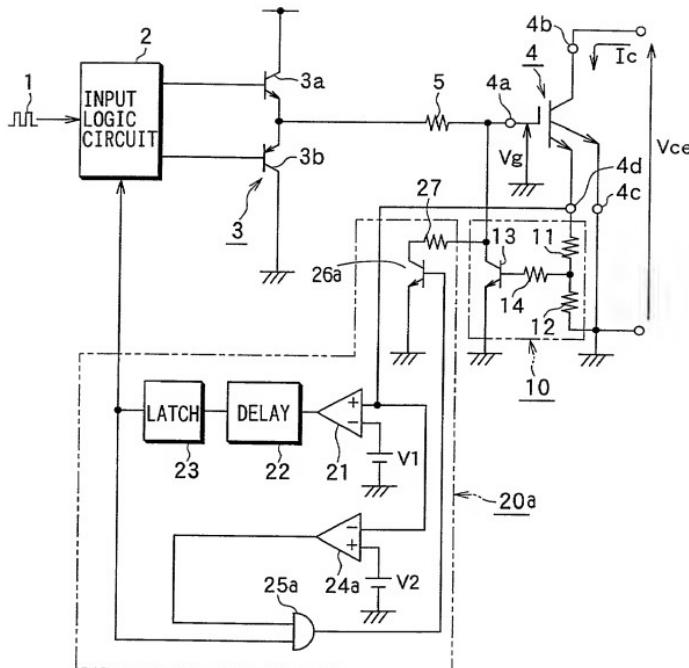


FIG. 1



06849715 • 332961

FIG. 2A

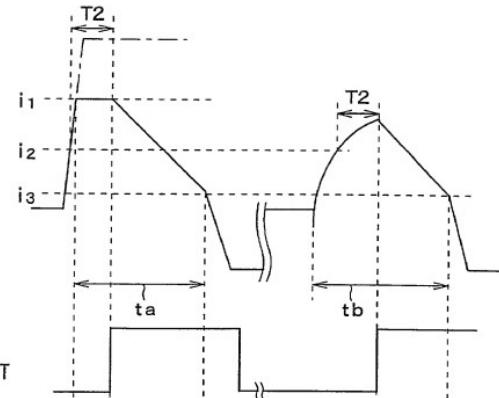


FIG. 2B

OUTPUT OF
LATCH CIRCUIT

FIG. 2C

V_g

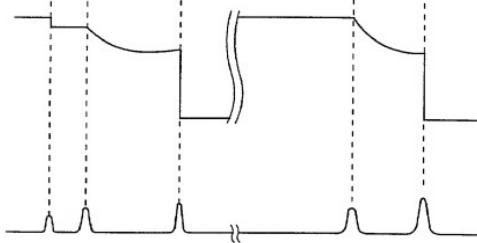
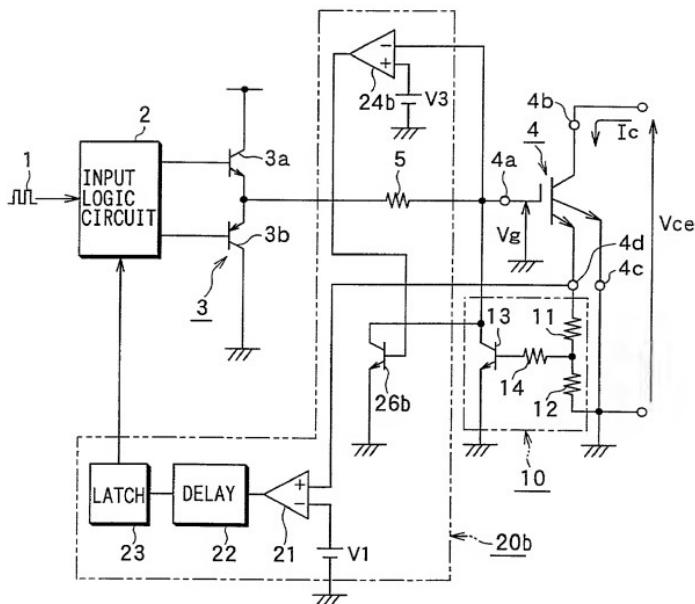


FIG. 2D

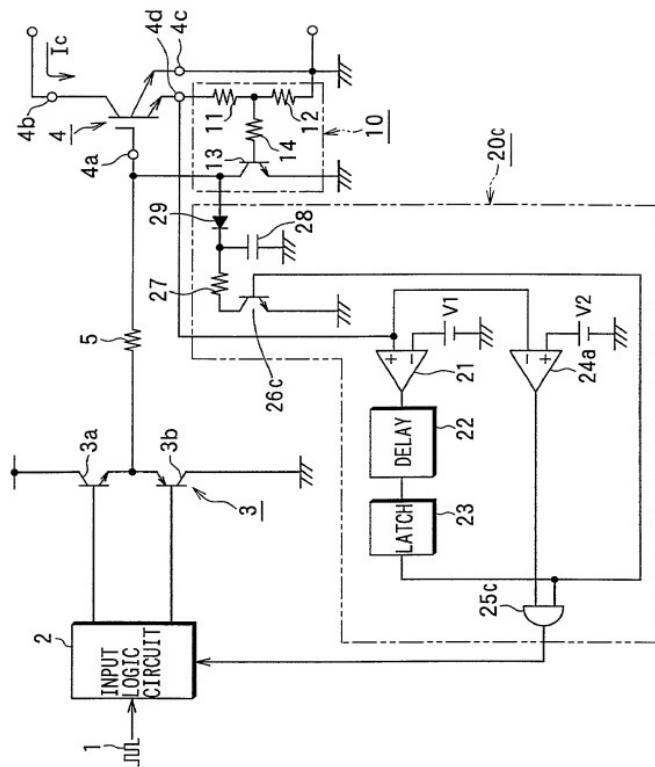
V_{ce}

FIG. 3



00629320557615

FIG. 4



09619745 032904

FIG. 5A

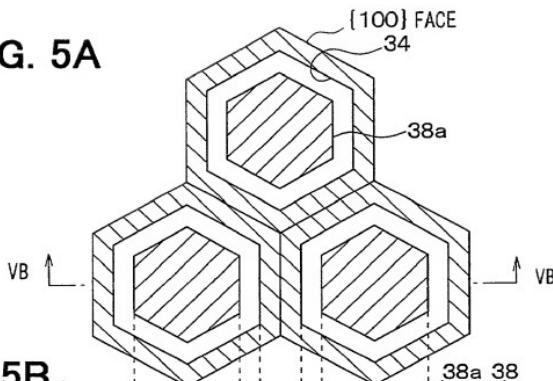


FIG. 5B

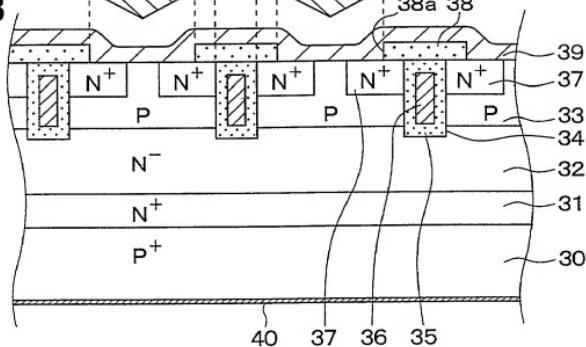


FIG. 6

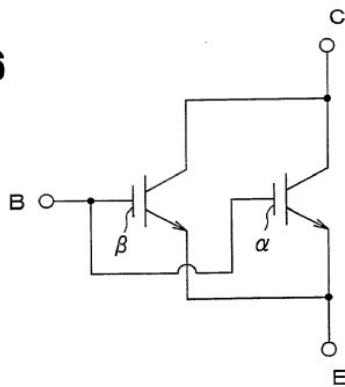


FIG. 7

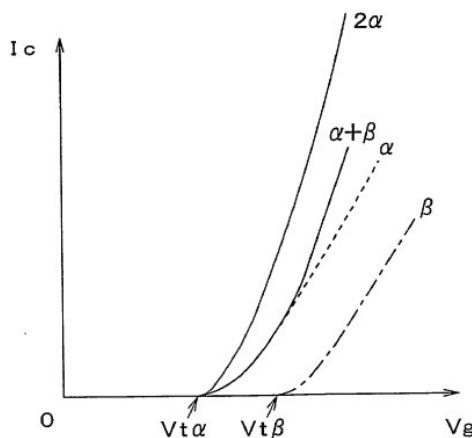
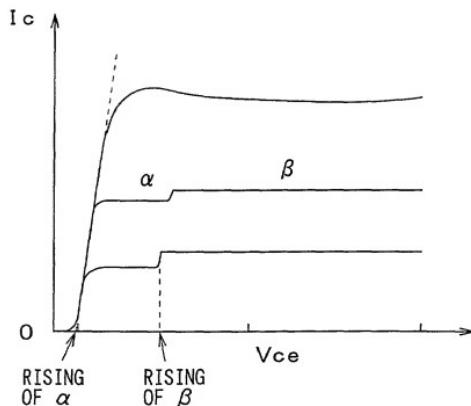


FIG. 8



Digitized by sscs901

FIG. 9A

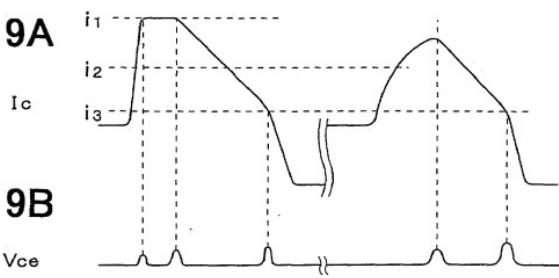


FIG. 9B

FIG. 10A

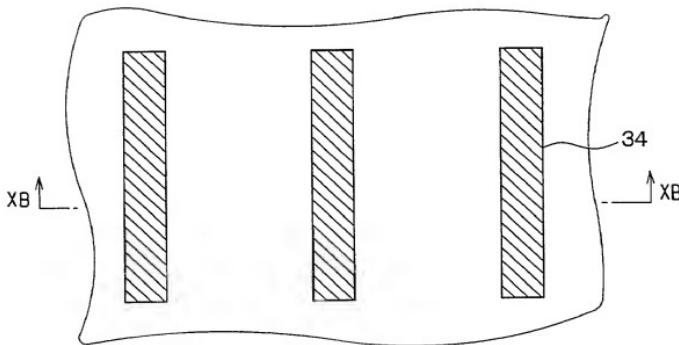


FIG. 10B

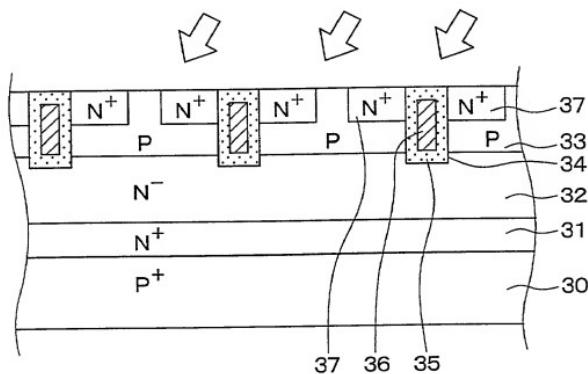


FIG. 11
PRIOR ART

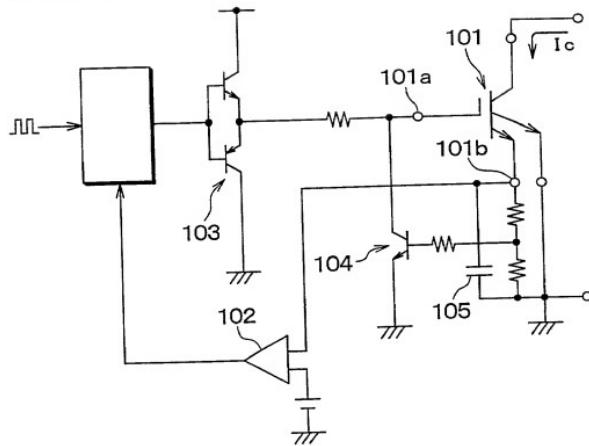


FIG. 12
PRIOR ART

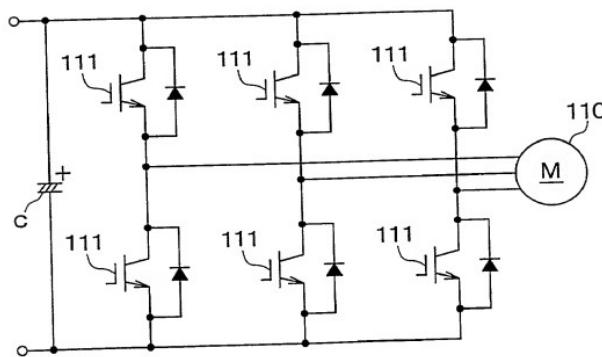


FIG. 13

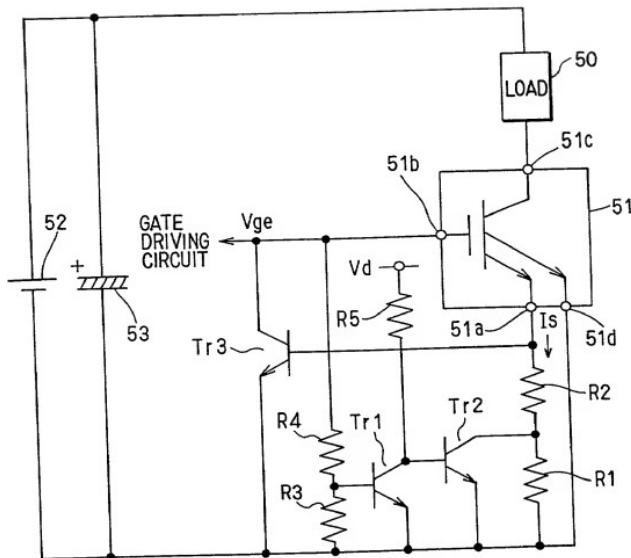


FIG. 14

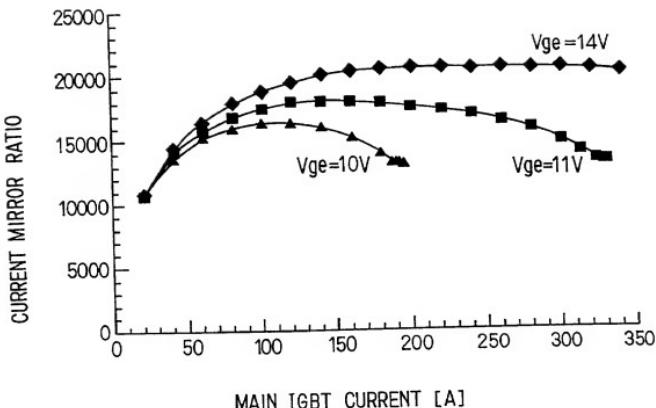


FIG. 15

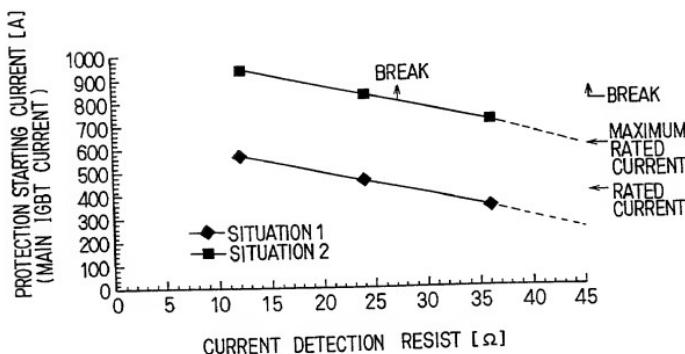


FIG. 16

